




# Abdelrahman Fouda

**Date of birth:** 26/05/2002

**Place of birth:** Dakahliya, Egypt


**Nationality:** Egyptian

## CONTACT

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## ABOUT ME

I am Abdelrahman Fouda, an engineering graduate with a strong background in electronics, nanoelectronics, and modern computer engineering. I recently completed my Bachelor's degree in Nanotechnology and Nanoelectronics, and I am now studying for a Master's Degree in Electronic Engineering at the University of Bologna.

During my studies, I gained solid experience in digital and analog circuit design, embedded systems, VLSI, RISC-V architecture, and FPGA/ASIC development. I enjoy working on practical engineering projects, solving problems, and learning new technologies. For my graduation project, I worked on improving RISC-V hardware extensions for cryptography, which strengthened my skills in computer architecture and hardware design.

I am motivated, organized, and always willing to support others, especially in technical subjects or project work.

## WORK EXPERIENCE

**KIDVITY** Egypt

**Business or Sector** Education | **Website** <https://kidvity.com/>

**Co-Founder**

**2024 – Current**

- Co-founded an educational toy startup focused on simple STEM projects for children.
- Helped with designing products, basic prototyping, testing ideas, and improving learning value.
- Worked on planning, teamwork, and explaining technical concepts in an easy way.

**Zewail City of Science and Technology** 6th October, Egypt

**Business or Sector** Information and communication

**Robotics and Tiny Machine Learning Engineering trainee**

**01/07/2024 – 19/10/2024**

- Designed and built an obstacle avoidance robot using Raspberry Pi Pico and radar sensors.
- Trained and deployed a quantized TensorFlow Lite model, achieving 83.9% accuracy on hardware.
- Transitioned Python-based models into C arrays for embedded system deployment.
- Utilized Q-learning for data collection and neural network training.
- Validated robot performance through real-world testing and simulation.

## EDUCATION AND TRAINING

**31/07/2025 – CURRENT** Bologna, Italy

**Laurea Magistrale in Ingegneria Elettronica (LM-29)** University of Bologna – Alma Mater Studiorum

**Address** Viale del Risorgimento 2, 40136 Bologna, Italy, Bologna, Italy |

**Website** <https://www.unibo.it/it> | **Field of study** Electronics and automation |

**Level in EQF** EQF level 7

**01/10/2020 – 30/06/2025** Giza, Egypt

**B.S.C., Nanotechnology and Nanoelectronics Engineering** University of Science and Technology in Zewail City

**Relevant Courses:** Digital Logic Design, Electric Circuits, Electronic Circuit Design, Solid State Devices, Physics of semiconductors, Advanced devices, C/C++ Programming Lab, Analog Integrated Circuit Design, Computer Architecture & Assembly Language, ASIC and FPGA Design, Real-Time Embedded System, Advanced Digital ASIC Design, Applied digital control, Nanofabrication Vacuum & Equipment Tech, Advanced RF/Mixed Signal ICs and Testing, Verification and Reliability.

**Software:** ModelSim, Cadence, Silvaco, CleWin, Proteus, Ansys, Autodesk Fusion, AutoCAD, COMSOL, Lumerical.

**Programming Languages:** Python, C, C++, MATLAB, Assembly.

**HDLs:** Verilog, SystemVerilog.

**Cleanroom:** Photolithography, Thermal Oxidation.

**Characterization Techniques:** XRD, AFM, TEM, FTIR.

**Website** <https://zewailcity.edu.eg/> | **Field of study** Electronics and automation | **Final grade** 3.0524/4.0000 |

**Level in EQF** EQF level 6 | **Thesis** RISC-V Integration and ISA Extension for Efficient MSM on BLS12-377 and BLS12-381 Curves

## LANGUAGE SKILLS

**MOTHER TONGUE(S):** Arabic

**Other language(s):**

English

**Listening** C1

**Spoken production** B2

**Reading** C1

**Spoken interaction** B2

**Writing** B2

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## SKILLS

COMSOL | Silvaco | Silvaco Tcad | Ansys | Autodesk Fusion 350 | Synopsys XA | FPGA based Digital Design/ Development and Testing | Verilog-HDL & VHDL

### ELECTRONICS

■ IC Mask Design : Cadence Virtuoso Product Suite | Photonic simulators including Lumerical FDTD

### PROGRAMMING LANGUAGES

Matlab/Simulink | Python | C / C++ / C+